Single Low Noise Operational Amplifier

The NE/SA/SE5534/5534A are single high-performance low noise operational amplifiers. Compared to other operational amplifiers, such as TL083, they show better noise performance, improved output drive capability, and considerably higher small-signal and power bandwidths.

This makes the devices especially suitable for application in high quality and professional audio equipment, in instrumentation and control circuits and telephone channel amplifiers. The op amps are internally compensated for gain equal to, or higher than, three. The frequency response can be optimized with an external compensation capacitor for various applications (unity gain amplifier, capacitive load, slew rate, low overshoot, etc.).

Features

- Small-Signal Bandwidth: 10 MHz
- Output Drive Capability: 600 Ω , 10 V_{RMS} at V_S = ± 18 V
- Input Noise Voltage: $4 \text{ nV}/\sqrt{\text{Hz}}$
- DC Voltage Gain: 100000
- AC Voltage Gain: 6000 at 10 kHz
- Power Bandwidth: 200 kHz
- Slew Rate: 13 V/µs
- Large Supply Voltage Range: ± 3.0 to ± 20 V
- Pb-Free Packages are Available

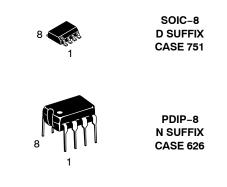
Applications

- Audio Equipment
- Instrumentation and Control Circuits
- Telephone Channel Amplifiers
- Medical Equipment

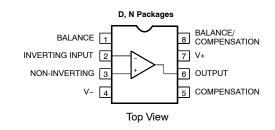


ON Semiconductor®

http://onsemi.com



PIN CONNECTIONS



DEVICE MARKING INFORMATION

See general marking information in the device marking section on page 8 of this data sheet.

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 8 of this data sheet.

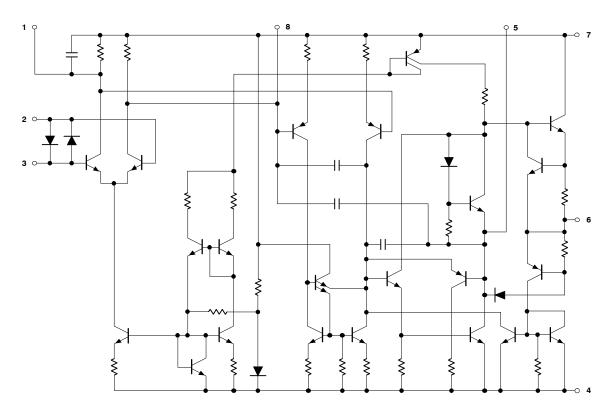


Figure 1. Equivalent Schematic

MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Supply Voltage		V _S	±22	V
Input Voltage		V _{IN}	$\pm V$ Supply	V
Differential Input Voltage (Note 1)		V _{DIFF}	±0.5	V
Operating Temperature Range NE SA SE		T _{amb}	0 to +70 -40 to +85 -55 to +125	°C
Storage Temperature Range		T _{stg}	-65 to +150	°C
Junction Temperature		Tj	150	°C
Power Dissipation at 25°C	N Package D Package	P _D	1150 750	mW
Thermal Resistance, Junction-to-Ambient	N Package D Package	$R_{ ext{ heta}JA}$	130 158	°C/W
Output Short-Circuit Duration (Note 2)		_	Indefinite	-
Lead Soldering Temperature (10 sec max)		T _{sld}	230	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Diodes protect the inputs against overvoltage. Therefore, unless current-limiting resistors are used, large currents will flow if the differential input

voltage exceeds 0.6 V. Maximum current should be limited to ± 10 mA. 2. Output may be shorted to ground at V_S = ± 15 V, T_{amb} = 25°C. Temperature and/or supply voltages must be limited to ensure dissipation rating is not exceeded.

			NE/S	SA5534/55	534A	SE	E5534/5534	4A	
Characteristic	Symbol	Test Conditions	Min	Тур	Max	Min	Тур	Мах	Unit
	V _{OS}		-	0.5	4.0	-	0.5	2.0	mV
Offset Voltage		Overtemperature	-	-	5.0	-	-	3.0	mV
	$\Delta V_{OS} / \Delta T$		-	5.0	-	-	5.0	_	μV/°C
	I _{OS}		-	20	300	-	10	200	nA
Offset Current		Overtemperature	-	-	400	-	-	500	nA
	$\Delta I_{OS} / \Delta T$		-	200	-	-	200	-	pA/∘C
	Ι _Β		-	500	1500	-	400	800	nA
Input Current		Overtemperature	-	-	2000	-	-	1500	nA
	$\Delta I_{B} / \Delta T$		-	5.0	-	-	5.0	_	nA/∘C
Supply Current Per Op Amp	Icc	Overtemperature		4.0 -	8.0 10		4.0 -	6.5 9.0	mA
Common Mode Input Range Common Mode Rejection Ratio Power Supply Rejection Ratio	V _{CM} CMRR PSRR		±12 70 -	±13 100 10	- - 100	±12 80 -	±13 100 10	- - 50	V dB μV/V
Large-Signal Voltage Gain	A _{VOL}	R _L ≥600 Ω,	25	100	-	50	100	-	V/mV
		$V_{O}^{-} = \pm 10 V$ Overtemperature	15	-	-	25	-	-	
Output Swing	V _{OUT}	$R_L \ge 600 \ \Omega$	±12	±13	-	±12	±13	-	V
		Overtemperature $R_L \ge 600 \Omega;$ $V_S = \pm 18 V$	±10 ±15	±12 ±16	-	±10 ±15	±12 16	-	
		$R_L \ge 2.0 \text{ k}\Omega$ Overtemperature	±13 ±12	±13.5 ±12.5	-	±13 ±12	± 13.5 ± 12.5	- -	
Input Resistance	R _{IN}		30	100	-	50	100	_	kΩ
Output Short Circuit Current	I _{SC}		-	38	-	-	38	_	mA

DC ELECTRICAL CHARACTERIST	ICS ($T_{amb} = 25^{\circ}C$; $V_{S} = \pm 15$ V, unless otherwise noted.) (Notes 3, 4 and 5)
DO LLEOTNICAL CHANACTENIST	$U = 1$ ($I_{amb} = 25$ C, $V_{S} = \pm 15$ V, unless of terwise noted.) (Notes 5, 4 and 5)

3. For NE5534/5534A, $T_{MIN} = 0^{\circ}C$, $T_{MAX} = 70^{\circ}C$. 4. For SA5534/5534A, $T_{MIN} = -40^{\circ}C$, $T_{MAX} = +85^{\circ}C$. 5. For SE5534/5534A, $T_{MIN} = -55^{\circ}C$, $T_{MAX} = +125^{\circ}C$.

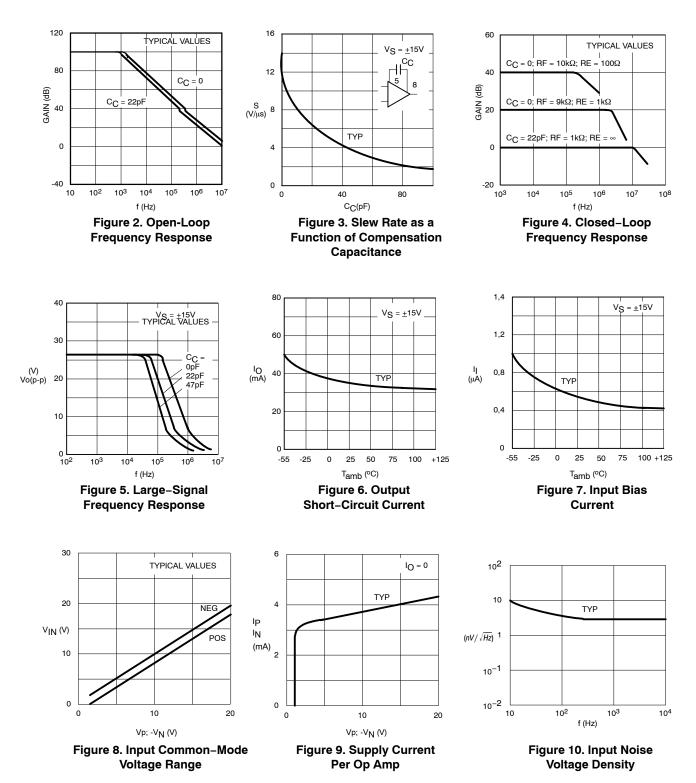
			NE/SA5534/5534A			SE5534/5534A			
Characteristic	Symbol	Test Conditions	Min	Тур	Max	Min	Тур	Max	Unit
Output Resistance	R _{OUT}	$\begin{array}{c} A_V = 30 \text{ dB} \\ \text{closed-loop} \\ \text{f} = 10 \text{ kHz}; \\ \text{R}_L = 600 \ \Omega; \\ \text{C}_C = 22 \text{ pF} \end{array}$	-	0.3	_	-	0.3	-	Ω
Transient Response		$\label{eq:Voltage-follower} \begin{array}{l} \mbox{Voltage-follower}, \\ \mbox{V}_{IN} = 50 \mbox{ mV} \\ \mbox{R}_L = 600 \ \Omega, \\ \mbox{C}_C = 22 \mbox{ pF}, \\ \mbox{C}_L = 100 \mbox{ pF} \end{array}$							
Rise Time	t _R		-	20	-	-	20	-	ns
Overshoot	-		-	20	-	-	20	-	%
Transient Response		$V_{\rm IN} = 50 \text{ mV}, \\ R_{\rm L} = 600 \ \Omega, \\ C_{\rm C} = 47 \text{ pF}, \\ C_{\rm L} = 500 \text{ pF}$							
Rise Time	t _R		-	50	-	-	50	-	ns
Overshoot	-		-	35	-	-	35	-	%
Gain	Av	$ f = 10 \text{ kHz}, C_C = 0 \\ f = 10 \text{ kHz}, \\ C_C = 22 \text{ pF} $	-	6.0 2.2	_		6.0 2.2		V/mV
Gain Bandwidth Product	GBW	C _C = 22 pF, C _L = 100 pF	-	10	-	-	10	-	MHz
Slew Rate	SR	C _C = 0 C _C = 22 pF	-	13 6.0	-	-	13 6.0	-	V/µs
Power Bandwidth	-	$V_{OUT} = \pm 10 \text{ V},$ $C_{C} = 0 \text{ pF}$	-	200	-	-	200	-	kHz
		$V_{OUT} = \pm 10 \text{ V},$ $C_{C} = 22 \text{ pF}$	-	95	-	-	95	-	
		$V_{OUT} = \pm 14 \text{ V}, \\ R_{L} = 600 \Omega, \\ C_{C} = 22 \text{ pF}, \\ V_{CC} = \pm 18 \text{ V}$	_	70	_	_	70	_	

AC ELECTRICAL CHARACTERISTICS ($T_{amb} = 25^{\circ}C$; $V_{S} = \pm 15$ V, unless otherwise noted.)

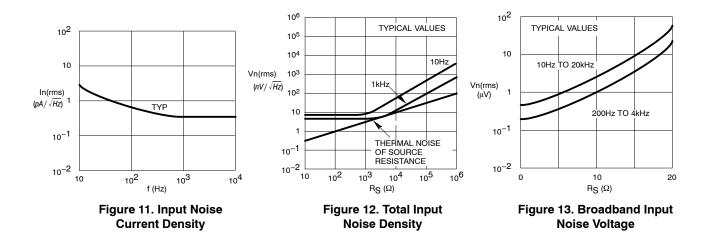
ELECTRICAL CHARACTERISTICS (T_{amb} = 25°C; V_S = 15 V, unless otherwise noted.)

			NE/SA/SE5534		NE/SA/SE5534A				
Characteristic	Symbol	Test Conditions	Min	Тур	Max	Min	Тур	Max	Unit
Input Noise Voltage	V _{NOISE}	f _O = 30 Hz f _O = 1.0 kHz		7.0 4.0		-	5.5 3.5	7.0 4.5	nV/√Hz
Input Noise Current	I _{NOISE}	f _O = 30 Hz f _O = 1.0 kHz	-	2.5 0.6	-	-	1.5 0.4	-	pA/√Hz
Broadband Noise Figure	-	f = 10 Hz to 20 kHz; R _S = 5.0 kΩ	-	-	-	-	0.9	-	dB
Channel Separation	-	f = 1.0 kHz; R _S = 5.0 kΩ	-	110	-	-	110	-	dB

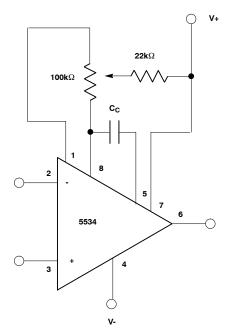
TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS



TEST LOAD CIRCUITS



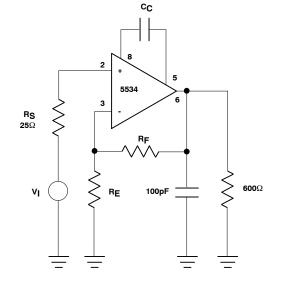


Figure 14. Frequency Compensation and Offset Voltage Adjustment Circuit

Figure 15. Closed-Loop Frequency Response

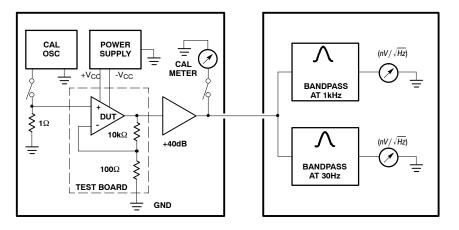
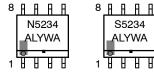


Figure 16. Noise Test Block Diagram

MARKING DIAGRAMS





8 8 8 8 8

S5234

ALYW

888

SOIC-8 D SUFFIX **CASE 751**

հ	Д	Д	Ч
1	NE52	234>	٨N
Þ		A٧	٧L
0	ΥY	ww	/G





PDIP-8 **N SUFFIX** CASE 626



= Blank or A х А = Assembly Location WL, L = Wafer Lot YY, Y = Year WW, W = Work Week

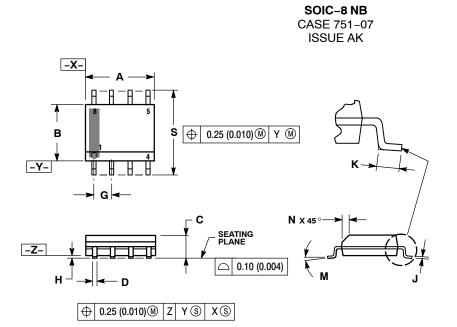
G or = Pb-Free Package

ORDERING INFORMATION

Device	Description	Temperature Range	Shipping [†]
NE5534AD	8-Pin Plastic Small Outline (SO-8) Package	0 to +70°C	98 Units / Rail
NE5534ADG	8-Pin Plastic Small Outline (SO-8) Package (Pb-Free)	0 to +70°C	98 Units / Rail
NE5534ADR2	8-Pin Plastic Small Outline (SO-8) Package	0 to +70°C	2500 / Tape & Reel
NE5534ADR2G	8-Pin Plastic Small Outline (SO-8) Package (Pb-Free)	0 to +70°C	2500 / Tape & Reel
NE5534AN	8-Pin Plastic Dual In-Line Package (PDIP-8)	0 to +70°C	50 Units / Rail
NE5534ANG	8-Pin Plastic Dual In-Line Package (PDIP-8) (Pb-Free)	0 to +70°C	50 Units / Rail
NE5534D	8-Pin Plastic Small Outline (SO-8) Package	0 to +70°C	98 Units / Rail
NE5534DG	8-Pin Plastic Small Outline (SO-8) Package (Pb-Free)	0 to +70°C	98 Units / Rail
NE5534DR2	8-Pin Plastic Small Outline (SO-8) Package	0 to +70°C	2500 / Tape & Reel
NE5534DR2G	8-Pin Plastic Small Outline (SO-8) Package (Pb-Free)	0 to +70°C	2500 / Tape & Reel
NE5534N	8-Pin Plastic Dual In-Line Package (PDIP-8)	0 to +70°C	50 Units / Rail
NE5534NG	8-Pin Plastic Dual In-Line Package (PDIP-8) (Pb-Free)	0 to +70°C	50 Units / Rail
SA5534AD	8-Pin Plastic Small Outline (SO-8) Package	–40 to +85°C	98 Units / Rail
SA5534ADG	8-Pin Plastic Small Outline (SO-8) Package (Pb-Free)	-40 to +85°C	98 Units / Rail
SA5534ADR2	8-Pin Plastic Small Outline (SO-8) Package	-40 to +85°C	2500 / Tape & Reel
SA5534ADR2G	8-Pin Plastic Small Outline (SO-8) Package (Pb-Free)	-40 to +85°C	2500 / Tape & Reel
SA5534AN	8-Pin Plastic Dual In-Line Package (PDIP-8)	-40 to +85°C	50 Units / Rail
SA5534ANG	8-Pin Plastic Dual In-Line Package (PDIP-8) (Pb-Free)	-40 to +85°C	50 Units / Rail
SA5534N	8-Pin Plastic Dual In-Line Package (PDIP-8)	-40 to +85°C	50 Units / Rail
SA5534NG	8-Pin Plastic Dual In-Line Package (PDIP-8) (Pb-Free)	-40 to +85°C	50 Units / Rail
SE5534AN	8-Pin Plastic Dual In-Line Package (PDIP-8)	–55 to +125°C	50 Units / Rail
SE5534ANG	8-Pin Plastic Dual In-Line Package (PDIP-8) (Pb-Free)	–55 to +125°C	50 Units / Rail
SE5534N	8-Pin Plastic Dual In-Line Package (PDIP-8)	–55 to +125°C	50 Units / Rail
SE5534NG	8-Pin Plastic Dual In-Line Package (PDIP-8) (Pb-Free)	–55 to +125°C	50 Units / Rail

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

PACKAGE DIMENSIONS

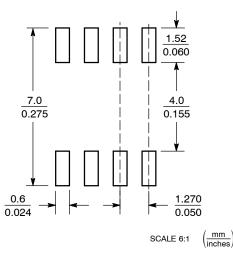


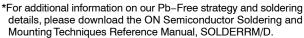
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: MILLIMETER.
 DIMENSION A AND B DO NOT INCLUDE MOLD PROTRUSION.
 MAXIMUM MOLD PROTRUSION 0.15 (0.006) DED SIDE
- MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.
 DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.
 751-01 THRU 751-06 ARE OBSOLETE. NEW STANDARD IS 751-07.

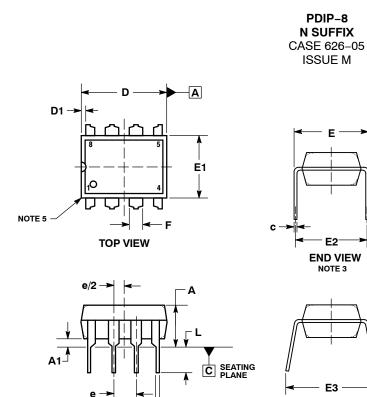
	MILLIN	IETERS	INC	HES				
DIM	MIN	MAX	MIN	MAX				
Α	4.80	5.00	0.189	0.197				
В	3.80	4.00	0.150	0.157				
С	1.35	1.75	0.053	0.069				
D	0.33	0.51	0.013	0.020				
G	1.27	.27 BSC 0.050 BS						
н	0.10	0.25	0.004	0.010				
J	0.19	0.25	0.007	0.010				
κ	0.40	1.27	0.016	0.050				
М	0 °	8 °	0 °	8 °				
Ν	0.25	0.50	0.010	0.020				
s	5.80	6.20	0.228	0.244				

SOLDERING FOOTPRINT*





PACKAGE DIMENSIONS



8X b

SIDE VIEW

🕀 0.010 🛞 C A

NOTES

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

- 2. CONTROLLING DIMENSION: INCHES. 3. DIMENSION E IS MEASURED WITH THE LEADS RE-
- STRAINED PARALLEL AT WIDTH E2. DIMENSION E1 DOES NOT INCLUDE MOLD FLASH. 4. 5.

ROUNDED CORNERS OPTIONAL.

		INCHES	;	MIL	LIMETE	RS		
DIM	MIN	NOM	MAX	MIN	NOM	MAX		
Α			0.210			5.33		
A1	0.015			0.38				
b	0.014	0.018	0.022	0.35	0.46	0.56		
С	0.008	0.010	0.014	0.20	0.25	0.36		
D	0.355	0.365	0.400	9.02	9.27	10.02		
D1	0.005			0.13				
Е	0.300	0.310	0.325	7.62	7.87	8.26		
E1	0.240	0.250	0.280	6.10	6.35	7.11		
E2	().300 BS	С	7.62 BSC				
E3			0.430			10.92		
е	0.100 BSC			2.54 BSC				
L	0.115	0.130	0.150	2.92	3.30	3.81		

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights of others. SCILLC products are not designed, intended, or authorized for use as components. in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws t is not for resale in any mann

END VIEW

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your loca Sales Representative