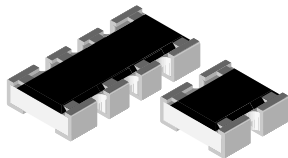


Thick Film, Resistor Array



FEATURES

- Convex terminal array with square corners
- 4 or 8 terminal package with isolated resistors
- Single component reduces board space and component counts
- Automatic placement capability
- Wave and solder paste reflow compatible
- Thick film resistance element
- Solderable wrap around termination
- Nickel barrier for inner electrode protection
- Standard E-24 resistance values
- Operating temperature range of - 55°C to + 150°C
- Consult factory for additional schematics, values, etc

STANDARD ELECTRICAL SPECIFICATIONS							
MODEL	POWER RATING $P_{70^{\circ}\text{C}}$ W	CIRCUIT	LIMITING ELEMENT VOLTAGE MAX. V_{\cong}	TEMPERATURE COEFFICIENT ppm/°C	TOLERANCE %	RESISTANCE RANGE Ω	E-SERIES
CRA04S	0.063	03	50	200	5	1R0 – 1M0	24
				100	2	10R – 1M0	24
				Jumper: Zero-Ohm-Resistor on request			

- Power rating depends on the max. temperature at the solder point, the component placement density and the substrate material
- Ask about extended value ranges.
- Packaging: according to EIA 481
- Contact factory for tighter tolerance.

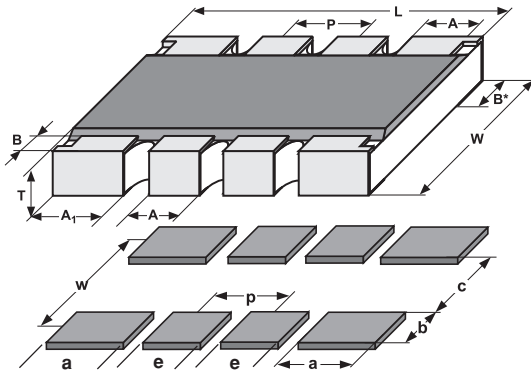
TECHNICAL SPECIFICATIONS		
PARAMETER	UNIT	CRA04S
Rated Dissipation at 70°C	W	0.063
Limiting Element Voltage ¹⁾	V_{\cong}	50
Insulation Voltage (1min)	$V_{\text{dc/ac peak}}$	100
Category Temperature Range	°C	- 55 / + 150
Insulation Resistance	Ω	> 10 ⁹

¹⁾Rated voltage: $\sqrt{P \times R}$

ORDERING INFORMATION					
CRA04S	08	03	473	J	RT7
MODEL	TERMINAL COUNT	CIRCUIT TYPE	RESISTANCE VALUE Ω	TOLERANCE \pm %	PACKAGING
	04, 08	03 only	First two digits (three for 1%) are significant. Last digit is the multiplier	J = \pm 5% G = \pm 2% Z = 0 Ω Jumper	Paper tape 10000pcs

8-Terminal device

S – Version



SOLDER PAD DIMENSIONS [in millimeters]						
	c	w	p	a	b	e
WAVE	0.45	1.0	0.5	0.4	0.5	0.3

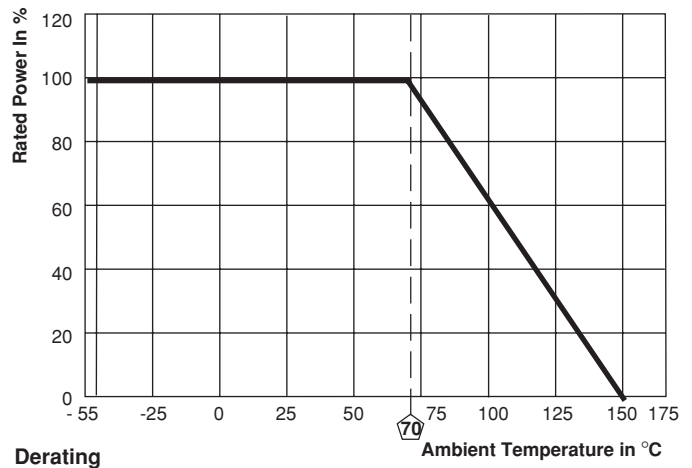
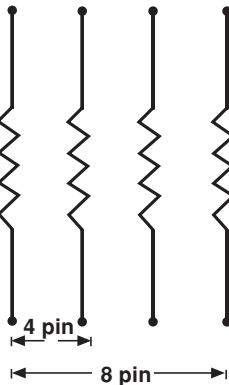
P dimension for the 4 pin is 0.65

The dimensions shown are for a 8 pin part. For parts with different pin numbers use the same pitch and add or subtract pads as required.

PIN NO#	DIMENSIONS [in millimeters]							
	L	A	A ₁	B	B*	P _{NOM}	T	W
4	1.0 ± 0.1	-	0.33	0.15	0.25	0.65	0.35	1.0
8	2.0 ± 0.2	0.30	0.4	0.15	0.25	0.50	0.35	1.0
TOL		± 0.15	± 0.15	± 0.10	± 0.1		± 0.1	± 0.15

CIRCUIT SCHEMATICS

03 Circuit CRA04S



PERFORMANCE		
TEST	CONDITIONS OF TEST	TEST RESULTS
Endurance Test at 70°C per EIA 575-3.14	1000 hours at 70°C, 1.5 hours "ON", 0.5 hours "OFF"	± 1.5%
Overload per EIA 575-3.6	Short time overload	± 0.5%
Thermal Shock	per EIA 575-3.5	± 0.5%
Moisture Resistance	per EIA 575-3.10	± 1.0%
Resistance to Soldering Heat EIA 575 3.8	10 seconds at 260°C solder bath temperature	± 2.0%
High Temperature Exposure	per EIA 575-3.7	± 1.0%
Low Temperature Operations	per EIA 575-3.6	± 0.5%
Solderability & Leaching	EIA 575-3.12	95% Coverage